

WHAT IS CLAIMED IS:

1. A low voltage differential signaling (LVDS) transmitting device, comprising:
 - a signal driving circuit for generating and outputting an analog image signal to an LVDS receiving device; and
 - a signal compensation circuit in communication with said signal driving circuit, generating a compensation signal in response to said analog image signal and a base signal, and feeding said compensation signal back to said signal driving circuit to adjust said analog image signal.
2. The LVDS transmitting device according to claim 1 wherein said signal compensation circuit comprises:
 - a voltage-to-current converting circuit for outputting said compensation signal as a current type in response to said analog image signal and said base signal; and
 - a first and a second current mirror circuits for feeding said compensation signal back to said signal driving circuit, wherein said first current mirror circuit is coupled to said voltage-to-current converting circuit and ground, and said second current mirror circuit is coupled to said first current mirror circuit, a power source and a differential signaling circuit of said signal driving circuit.
3. The LVDS transmitting device according to claim 2 wherein said voltage-to-current converting circuit outputs said compensation signal according to a voltage difference between said analog image signal and base signal.
4. The LVDS transmitting device according to claim 2 wherein said signal driving circuit comprises a first and a second current sources and a differential signaling circuit, wherein said first current source comprises a transistor coupled between a power source and said differential signaling device, said second current source comprises at least two transistors coupled

between said differential signaling device and ground, and said differential signaling circuit comprises at least four transistors.

5. The LVDS transmitting device according to claim 4 wherein said compensation signal fed back to said signal driving circuit compensates the current signal variation of said first current source so as to stabilize said analog image signal generated by said signal driving circuit.
6. The LVDS transmitting device according to claim 2 wherein each of said first and said second current mirror circuits includes two transistors.
7. The LVDS transmitting device according to claim 2 wherein said signal compensation circuit further comprises a first and a second shunting resistors electrically connected between said signal driving circuit and said voltage-to-current converting circuit for shunting said analog image signal before said analog image signal is transmitted to said voltage-to-current converting circuit.
8. The LVDS transmitting device according to claim 1 wherein said signal driving circuit is electrically connected to two serially connected resistors of said LVDS receiving device for transmitting said analog image signal with a swing of 300 mV ~350 mV to said LVDS receiving device.
9. The LVDS transmitting device according to claim 1 wherein said base signal is a band-gap voltage signal generated by a band-gap circuit.
10. A compensation circuit for use with a signal driving circuit of a low voltage differential signaling (LVDS) transmitting device, comprising:
 - a signal converting circuit receiving an analog image signal from said signal driving circuit, and generating a compensation signal according to a voltage difference between said analog image signal and a base signal; and
 - a current mirror circuit in communication with said signal driving circuit

and said signal converting circuit, feeding said compensation signal back to said signal driving circuit for compensating signal variation of said signal driving circuit.

11. The compensation circuit according to claim 10 wherein said signal converting circuit is a voltage-to-current converting circuit for outputting said compensation signal as a current type.
12. The compensation circuit according to claim 10 wherein said base signal is a constant voltage signal generated by a band-gap circuit.
13. The compensation circuit according to claim 10 wherein said current mirror circuit includes a first and a second sub-circuits, each comprising two transistors.
14. The compensation circuit according to claim 11 wherein said first sub-circuit is coupled to said signal converting circuit and ground, and said second sub-circuit is coupled to said first sub-circuit, a power source and a differential signaling circuit of said signal driving circuit.
15. The compensation circuit according to claim 14 wherein said compensation signal fed back to said signal driving circuit compensates the current signal variation of a specified current source, which is electrically connected between said power source and said differential signaling circuit, so as to stabilize said analog image signal generated by said signal driving circuit.
16. The compensation circuit according to claim 15 wherein said specified current source includes a transistor.
17. The compensation circuit according to claim 10 further comprising a first and a second shunting resistors electrically connected between said signal driving circuit and said signal converting circuit for shunting said analog image signal before said analog image signal is transmitted to said signal

converting circuit.

18. A method for stabilizing an analog image signal outputted from a signal driving circuit of an low voltage differential signaling (LVDS) transmitter to an LVDS receiver, comprising steps of:

shunting said analog image signal;

generating a compensation signal according to a voltage difference between said shunted analog image signal and a base signal; and

feeding said compensation signal to said signal driving circuit to compensate said analog image signal via a mirroring operation between a current source and said compensation signal.

19. The method according to claim 18 wherein said base signal is a constant band-gap voltage signal.